

## **Dr. K. POULOSE JACOB, the Computer Science veteran**

Dr. K. Poulouse Jacob, is the former Vice Chancellor (officiating) and Pro Vice Chancellor of Cochin University of Science and Technology (CUSAT). He served CUSAT in different positions for over thirty five years; almost 20 years as Professor & Head of the Department of Computer Science. During the period he also served as Dean – Faculty of Engineering, Dean – Faculty of Technology and Chairman of five different Boards of Studies. Several administrative responsibilities like Director –Strategic Planning, Director – Centre for Information Resource Management, Head of the Department of Computer Applications were also held by him at CUSAT. The Cusat intranet, as well as the site, [www.cusat.ac.in](http://www.cusat.ac.in) and the webmail were his initiatives. He has been Member of the Standing Committee on Computer Education & Development of the University Grants Commission; he has also served in several other Expert committees at the National level, which includes the DRDO, UPSC, NAAC, NBA and in Statutory Committees of the state government.

Dr. Jacob received Ph D in Computer Engineering and M.Tech in Electronics from CUSAT, while the Bachelor's in Electrical Engineering he received with National Merit Scholarship, from Kerala University. He had advanced training in the Indian Institute of Science, Bangalore, the Pink Elephant, Reading, UK, and the Management Development Foundation, Ede, the Netherlands.

He has undertaken several projects funded by DoE, MHRD, DST, TIFAC and the state government. He has been the Project Director for an Infrastructure Project funded by the Royal Dutch Government involving four Universities in the Netherlands, with the objective of augmenting Research and teaching in identified areas at CUSAT. He has travelled extensively visiting institutions of higher learning in South-East Asia, Europe, USA, Central America, UK, Africa and the UAE.

He has delivered invited lectures at several Universities and Higher Education Institutions around the world. He has more than 130 research papers published in International peer reviewed journals and conferences. His research areas cover several aspects of Computer Science like Intelligent Architectures, Networks, WSN, Information Systems Engg, Cyber Security etc. He has edited seven volumes in Computer Science and written several book chapters. Nineteen scholars have secured their PhD under his supervision, as on 2017; two more have submitted their dissertation.

He has received the Outstanding Service Award of the IEEE COMPUTER SOCIETY, Chapter Patron award of the Computer Society of India and Certificate of Recognition from the University of San Carlos in the Philippines. A senior Professional member of the ACM, he now occupies the ACM Chapter Chair at Cochin. He has been selected as [2017 Albert Nelson Marquis Lifetime Achievement Award](#) Winner.

Currently he officiates as the NeST Chair Professor at the Rajagiri School of Engineering and Technology, Cochin, a premier institution offering excellence in engineering education and research; while serving as Director of the NeST Knowledge Centre, the constituent of a corporate having DST approved R&D wing. He also heads the Scientific Computing Panel of the Naval Research Board under the Defense Research & Development Organisation of the Ministry of Defense, Government of India. He is active in various socio-cultural activities as well.

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