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**Dr. K. POULOSE JACOB**, Professor of Computer Science at Cochin University of Science and Technology since 1994, is currently the Pro Vice Chancellor.

A National Merit Scholar all through, Dr. Jacob entered the service of Cochin University as a Lecturer in 1983, after being part of the R&D team of a Computer peripheral manufacturing industry, for one year. He has been teaching the B.Tech students of the University since 1980. He was the first PhD in Computer Engineering from the Cochin University of Science and Technology [CUSAT]. He was elevated as Reader in Computer Science in 1989 and subsequently Professor in 1994.

He is the founder Director of the School of Computer Science Studies. The MCA programme of CUSAT currently administered at the Department of Computer Applications, a constituent of the School of Computer Science Studies, has been well nurtured and put in the right track during its formative days through his stewardship. Two new M.Tech programmes were started in the Department of Computer Science, also a constituent of the School, under his leadership. The Information Technology branch in undergraduate Engineering programme at the national level was conceived by the expert group led by him. He has initiated the M.Tech program in the branch of Computer Science & Engineering in the neighbouring Mahatma Gandhi University, as the designated Convener of the Core-committee. He has initiated the development of several new laboratories; the CUSAT intranet was conceived and commissioned under his leadership. The CUSAT website [www.cusat.ac.in](http://www.cusat.ac.in) was the result of his initiative. He has been Director of the CUSAT Computer Centre and had initiated the Centre for Information Resource Management, which he had been steering all along. He has been the Dean of the Faculty of Engineering and Chairman, Board of Studies in Computer Science as well as Chairman, Board of Studies in Computer Applications. He has also been Chairman, Board of Studies in Engineering. He has also served as Dean of the Faculty of Technology and as Member of the CUSAT Syndicate. As member of Academic Council of Cochin University continuously since 1994, he is the senior-most member in the Council. He was elected to the University Senate, repeatedly for a continuous term of 12 years.

He has held additional administrative responsibilities at CUSAT, like Head of the Department of Computer Applications. He has been Director of the Centre for MHO Co-operation, which was managing the 150 million

Indian rupee funding assistance from the Dutch Government, for improving standards in identified areas of teaching and research in this University. As Director - Strategic Planning, he had played the key role in preparing the DPR submitted to the MHRD, with respect to the upgradation of CUSAT to an institution of national importance. The designated expert committee approved the DPR and recommended financial support to the tune of Rs.600 crores. As Director, CUSAT Planning & Development, he led the team that was instrumental in preparing the XII plan document of CUSAT submitted to the University Grants Commission.

His research interests are mainly in Intelligent Architectures, Networks, Information Systems Engineering, and Artificial Intelligence. He has more than 120 publications to his credit, listed in Google Scholar. Eighteen candidates have earned their Doctoral degree under his guidance. He is PhD examiner for eight other Universities. He has travelled abroad extensively, visiting institutions of repute and participating in conferences at places in the USA, UK, Europe, Australia, Africa, Malaysia, Singapore and the UAE.

As a member of the Standing Committee of the University Grants Commission, he was part of policy making in Computer education & development at the national level. He is in the Governing Council of three institutions of higher learning in Engineering.

He had officiated as Vice Chancellor of CUSAT during January – October, 2014. The CUSAT Seminar Complex, having a theatre type air-conditioned auditorium with 700 seats was completed and inaugurated during this period. Faculty appointments were resumed after a long interregnum, and the tempo created for continuance of the process. The University Registrar was appointed while he was the VC. CUSAT could become a partner in the South Asia Anglia partnership, a programme of the Anglia Ruskin University, United Kingdom, partly supported by the British Council, which was an outcome of his initiative; the other partners being legendary institutions like the University of Mumbai, University of Mysore etc. He had recently delivered an invited talk on "Government, Governance and Growth: a constant juggle" at the leadership conference in the UK, hosted by the partnership.

He has been nominated as the Head of the Panel on Scientific Computing of the Naval Research Board under the Defence Research & Development Organisation (DRDO), Government of India.

Dr. K. Poullose Jacob is a Professional member of the ACM (Association for Computing Machinery) and a Life Member of the Computer Society of India. Listed in the 2010 Edition of Who's Who in the World® published by Marquis Who's Who, he figures as a biographical reference in that Volume, which lists the world's most accomplished individuals.

